

**PRODUCT CHANGE NOTICE**

1. TITLE UT65CML8X8FD CROSSPOINT SWITCH (XPS) - PACKAGE PINOUT DIAGRAM CORRECTION.		2. DOCUMENT NUMBER SPO-2022-PCN-001	
		3. DATE (Year, Month, Date) 2022 JULY 26	
4. MANUFACTURER NAME AND ADDRESS CAES 4350 CENTENNIAL BOULEVARD COLORADO SPRINGS, COLORADO 80907-3486		5. MANUFACTURER POINT OF CONTACT NAME Bruce Massey	
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8. CAGE CODE <b>65342</b>	9. EFFECTIVE DATE 2022 MAY 03	10. PRODUCT IDENTIFICATION CODE WQ04	11. BASE PART UT65CML8X8FD
12. BLANK		13. SMD NUMBER 5962-17213	14. DEVICE TYPE DESIGNATOR 01, 02
		15. RHA LEVELS R	16. QML LEVEL Q, V
		17. NON QML LEVEL Prototype, HiRel	18. GIDEP <b>GB4-C-22-001</b>
19. PRODUCT CHANGE The UT65CML8X8FD Crosspoint Switch (XPS) Data Sheet (DS) and SMD documents were recently found to contain an incorrect Package Pinout Diagram. Correction: G11=TXP_3_3, G12=TXN_3_3. Please reference the following Figures and Tables from these documents:  <u>Data Sheet (DS):</u> 2.1 Pinlist – Table Format: Table 2: Pin List – Pin Description – Table Format (Continued), page 14 2.2 Pinlist – Package Configuration: Top View, Table 3. Pin List / Package Configuration, page 17  <u>SMD:</u> FIGURE 2. <u>Terminal PIN number (TOP VIEW)</u> , SHEET 14 FIGURE 2. <u>Terminal PIN number – continued.</u> , SHEET 16.  For applicable part numbers, see this PCN, Table 1, SHEET 4, below.			
20. DISPOSITIONARY RECOMMENDATION:		CHECK & USE AS IS <input type="checkbox"/>	CONTACT MANUFACTURER <input type="checkbox"/>
		REMOVE & REPLACE <input type="checkbox"/>	CORRECT & <input checked="" type="checkbox"/> USE AS SPECIFIED

DS: 2.2 Pinlist – Package Configuration: Top View, Table 3. Pin List / Package Configuration (Corrected)

	12	11	10	9	8	7	6	5	4	3	2	1	
M	VSS	VDDA TX_3	TXP _0_3	TXN _0_3	VSS	VDD TX_3	TXN _3_2	TXP _3_2	VSS	VDD TX_2	TXN _2_2	TXP _2_2	M
L	TXP _1_3	TXN _1_3	VSS	VSS	RXP _0_3	RXN _0_3	VSS	VSS	RXN _2_2	RXP _2_2	VSS	VDDA TX_2	L
K	VDDA RX_3	VSS	RXP _1_3	RXN _1_3	VSS	VDD	RXN _3_2	RXP _3_2	VSS	VDD	TXN _1_2	TXP _1_2	K
J	TXP _2_3	TXN _2_3	VDD	VSS	RXP _2_3	RXN _2_3	VSS	VSS	RXN _1_2	RXP _1_2	VSS	VDDA RX_2	J
H	VDD RX_3	VSS	RXP _3_3	RXN _3_3	VSS	VDD _25	VDDA _BIAS _1	VDD	VSS	VSS	TXN _0_2	TXP _0_2	H
G	TXN _3_3	TXP _3_3	VDD _25	VSS	SPI cs_n	MIS _out	VSS	VSS	RXN _0_2	RXP _0_2	VSS	VDD RX_2	G
F	VDD RX_1	VSS	RXP _0_1	RXN _0_1	SPI clock	MOS _in	VSS	N/C	VSS	VSS	TXN _3_0	TXP _3_0	F
E	TXP _0_1	TXN _0_1	VDD	VSS	VDD _25	VDD	VDDA _BIAS _0	VSS	RXN _3_0	RXP _3_0	VSS	VDD RX_0	E
D	VDDA RX_1	VSS	RXP _1_1	RXN _1_1	VSS	VSS	RXN _2_0	RXP _2_0	VSS	VSS	TXN _2_0	TXP _2_0	D
C	TXP _1_1	TXN _1_1	VDD	VSS	RXP _3_1	RXN _3_1	VDD	VSS	RXN _1_0	RXP _1_0	VSS	VDDA RX_0	C
B	VDDA TX_1	VSS	RXP _2_1	RXN _2_1	VSS	VSS	RXP _0_0	RXN _0_0	VSS	VSS	TXN _1_0	TXP _1_0	B
A	TXP _2_1	TXN _2_1	VSS	VDD TX_1	TXP _3_1	TXN _3_1	VDD TX_0	VSS	TXN _0_0	TXP _0_0	VDDA TX_0		A
	12	11	10	9	8	7	6	5	4	3	2	1	

<https://caes.com/sites/default/files/documents/Datasheet-UT65CML8X8FD.pdf>

3.125 Gbps Crosspoint Switch (XPS) UT65CML8X8FD

DATA SHEET

RELEASED 03/23/22

SMD: FIGURE 2. Terminal PIN number (TOP VIEW) (Corrected)

	12	11	10	9	8	7	6	5	4	3	2	1	
M	VSS	VDDA TX_3	TXP _0_3	TXN _0_3	VSS	VDD TX_3	TXN _3_2	TXP _3_2	VSS	VDD TX_2	TXN _2_2	TXP _2_2	M
L	TXP _1_3	TXN _1_3	VSS	VSS	RXP _0_3	RXN _0_3	VSS	VSS	RXN _2_2	RXP _2_2	VSS	VDDA TX_2	L
K	VDDA RX_3	VSS	RXP _1_3	RXN _1_3	VSS	VDD	RXN _3_2	RXP _3_2	VSS	VDD	TXN _1_2	TXP _1_2	K
J	TXP _2_3	TXN _2_3	VDD	VSS	RXP _2_3	RXN _2_3	VSS	VSS	RXN _1_2	RXP _1_2	VSS	VDDA RX_2	J
H	VDD RX_3	VSS	RXP _3_3	RXN _3_3	VSS	VDD _25	VDDA _BIAS _1	VDD	VSS	VSS	TXN _0_2	TXP _0_2	H
G	TXN _3_3	TXP _3_3	VDD _25	VSS	SPI cs_n	MIS _out	VSS	VSS	RXN _0_2	RXP _0_2	VSS	VDD RX_2	G
F	VDD RX_1	VSS	RXP _0_1	RXN _0_1	SPI clock	MOS _in	VSS	N/C	VSS	VSS	TXN _3_0	TXP _3_0	F
E	TXP _0_1	TXN _0_1	VDD	VSS	VDD _25	VDD	VDDA _BIAS _0	VSS	RXN _3_0	RXP _3_0	VSS	VDD RX_0	E
D	VDDA RX_1	VSS	RXP _1_1	RXN _1_1	VSS	VSS	RXN _2_0	RXP _2_0	VSS	VSS	TXN _2_0	TXP _2_0	D
C	TXP _1_1	TXN _1_1	VDD	VSS	RXP _3_1	RXN _3_1	VDD	VSS	RXN _1_0	RXP _1_0	VSS	VDDA RX_0	C
B	VDDA TX_1	VSS	RXP _2_1	RXN _2_1	VSS	VSS	RXP _0_0	RXN _0_0	VSS	VSS	TXN _1_0	TXP _1_0	B
A	TXP _2_1	TXN _2_1	VSS	VDD TX_1	TXP _3_1	TXN _3_1	VDD TX_0	VSS	TXN _0_0	TXP _0_0	VDDA TX_0		A
	12	11	10	9	8	7	6	5	4	3	2	1	

<https://landandmaritimeapps.dla.mil/Downloads/MilSpec/Smd/17213.pdf>

MICROCIRCUIT, DIGITAL, RADIATION HARDENED, LOW VOLTAGE, CMOS, CML LOGIC, 8X8 FULL DUPLEX CROSSPOINT SWITCH (XPS), MONOLITHIC SILICON

5962-17213

SMD corrections are in progress with ECD of May, 2022.

Table 1. Affected Part Numbers Under This PCN

Description	SMD Number	CAES Part Number	CAES PIC Number
UT65CML8X8FD Crosspoint Switch (XPS)	5962R1721301QXC	UT65CML8X8FDRZQC	WQ04
	5962R1721301QYF	UT65CML8X8FDRSQF	WQ04
	5962R1721302QXC	UT65CML8X8FDRZZC	WQ04
	5962R1721302QYF	UT65CML8X8FDRSZF	WQ04
	5962R1721301VXC	UT65CML8X8FDRZVC	WQ04
	5962R1721301VYF	UT65CML8X8FDRSVF	WQ04